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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/562,293	12/22/2005	Peter W. Green	GB02 0136 US	3260
24738	7590	12/20/2006	EXAMINER	
PHILIPS ELECTRONICS NORTH AMERICA CORPORATION			TOBERGTE, NICHOLAS J	
INTELLECTUAL PROPERTY & STANDARDS				
1109 MCKAY DRIVE, M/S-41SJ			ART UNIT	PAPER NUMBER
SAN JOSE, CA 95131			2823	
SHORTENED STATUTORY PERIOD OF RESPONSE		MAIL DATE	DELIVERY MODE	
3 MONTHS		12/20/2006	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/562,293	GREEN, PETER W.
	<b>Examiner</b>	<b>Art Unit</b>
	Nicholas J. Tobergte	2823

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 05 December 2006.
- 2a) This action is FINAL.                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 16-21 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 16-21 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All    b) Some \* c) None of:
  1. Certified copies of the priority documents have been received.
  2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)            | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____.                                     |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>12/22/05</u> .  | 6) <input type="checkbox"/> Other: _____.                         |

**DETAILED ACTION**

***Election/Restrictions***

Applicant's election without traverse of claims 16-21 in the reply filed on 12/5/06 is acknowledged.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 16-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Yamazaki et al (US 6,501,094).

Pertaining to claim 16, Yamazaki teaches a TFT comprising a substrate a gate overlying the substrate **101** and having side edges inclined towards one another, a channel region overlying the gate and source and drain regions **113 and 114** overlying said side edges respectively, wherein the gate has been formed on the substrate by an etching process that involved formation of a tip in an apex region between the side edges or a radius of a few nanometers.

Pertaining to claim 17, Yamazaki teaches a TFT according to claim 16, wherein the tip was removed before the channel region was applied. Method limitations are not

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patentable when the invention is drawn to a device. Since Yamazaki teaches the end structure, then it meets the claims. How the structure was formed is irrelevant.

Pertaining to claim 18, Yamazaki teaches a TFT according to claim 16, wherein the gate **100** is overlaid by a layer of insulating material **105**, the channel region overlies the insulating material **105**, a layer of doped semiconductor material **133 and 114** overlies the channel region, and a layer of conductive material **116 and 117** from which said source and drain regions have been formed, overlies the doped semiconductor material.

Pertaining to claim 19, Yamazaki teaches a TFT according to claim 16 wherein the channel region comprises intrinsic amorphous silicon **107**.

Pertaining to claim 21, Yamazaki teaches a TFT according to claim 18, wherein the doped semiconductor material **113 and 114** comprises n doped silicon.

#### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yamazaki as applied to claims 16-19 above.

Pertaining to claim 20, Yamazaki teaches a TFT according to claim 18, but fails to teach wherein the insulating layer is silicon nitride. Yamazaki teaches an insulating layer of silicon oxide. However it would have been obvious to one of ordinary skill in the art to substitute a known insulative material such as silicon nitride, with that of silicon oxide. Silicon nitride is a notoriously well-known insulator in the art for which it pertains. See also In re Leshin, 227 F.2d 197, 125 USPQ 416 (CCPA 1960) (selection of a known plastic to make a container of a type made of plastics prior to the invention was held to be obvious)

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nicholas J. Tobergte whose telephone number is 571-272-6006. The examiner can normally be reached on Mon - Thur 7am - 5:30pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

NJT



FERNANDO L. TOLEDO  
PRIMARY PATENT EXAMINER